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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/057,923	01/29/2002	Masatoshi Yasunaga	50090-470	3734

7590 04/10/2002

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Washington, DC 20005-3096

EXAMINER

NGUYEN, DILINH P

ART UNIT	PAPER NUMBER
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2814

DATE MAILED: 04/10/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/057,923

Applicant(s)

YASUNAGA, MASATOSHI

Examiner

DiLinh Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 January 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-12 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 3. 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hembree (U.S. Pat. 6117797) in view of Tao et al. (U.S. Pat. 6229702).

Hembree discloses a semiconductor device (fig. 3F, column 3, lines 50-63 and column 5, lines 64 et seq.) comprising:

a substrate 20;

a semiconductor chip 12 mounted on the substrate;

a sealing member 48 for encapsulating the semiconductor chip on the substrate;

and

a heat sink plate 30 fixed by the sealing member, wherein the heat sink plate has concavo-convex portions formed on an exposed surface thereof and is disposed so as to be opposed to a main surface on which semiconductor elements of the semiconductor chip are formed.

Hembree discloses the claimed invention except for showing a method of making external connection to the device.

Tao et al. show a technique for making external connections which show a wire bonding connection to the substrate as Hembree but also show the connection to ball

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bond on the opposite side of the substrate. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Hembree to provide a means for making external connections for the device as shown by Tao et al.

- Regarding claim 2, Hembree discloses the heat sink plate is formed so that the convex portions protrude from the surface of the sealing member to the outside.
- Regarding claim 3, Hembree discloses the heat sink plate is disposed so as to adjoin the main surface with a thin sealing member 34 placed on the main surface of the semiconductor chip being interposed therebetween.
- Regarding claim 4, Hembree discloses the heat sink plate is formed so that the convex portions protrude from the surface of the sealing member to the outside.
- Regarding claim 5, Hembree discloses the heat sink plate is disposed so as to make contact with the main surface used for the semiconductor elements.
- Regarding claim 6, Hembree discloses the heat sink plate is formed so that the convex portions protrude from the surface of the sealing member to the outside.

3. Claims 7 and 10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Schneider et al. (U.S. Pat. 5610442) in view of Tao et al. (U.S. Pat. 6229702).

Schneider et al. disclose a semiconductor device (fig. 4, column 5, lines 65 et seq.) comprising:

a substrate 102;

a semiconductor chip 306 mounted on the substrate;

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a sealing member 404 for encapsulating the semiconductor chip on the substrate; and

a heat sink plate 414 fixed by the sealing member, wherein the heat sink plate has a heat dissipation fin formed integrally therewith.

Schneider et al. disclose the claimed invention except for showing a method of making external connections to the device.

Tao et al. show a technique for making external connections which show a wire bonding connection to the substrate as Schneider et al. but also show the connection to ball bond on the opposite side of the substrate. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Schneider et al. to provide a means for making external connections for the device as shown by Tao et al.

- Regarding claim 10, Schneider et al. disclose the heat sink plate is disposed so as to be opposed to a main surface on which semiconductor elements of the semiconductor chip are formed.

4. Claims 8-9 and 11-12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Schneider et al. in view of Tao et al. and further in view of Suzuki et al. (U.S. Pat. 6278609).

Schneider et al. and Tao et al. disclose the claimed invention except for not specifically point out the heat sink plate and the heat dissipation fin have engaging portions brought into engagement with each other.

Suzuki et al. disclose a semiconductor device (cover fig., column 3, lines 3 et seq.) comprising:

An IC card slot 1 and a socket 2 have engaging portions brought into engagement with each other, wherein the engaging portions are respectively formed at the IC card slot and the socket comprise a guide rail member 2c for guiding the IC card. It would have been obvious to modify the device of Schneider et al. and Tao et al. by the method of Suzuki et al. to insert the heat dissipation fin in the heat sink plate.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (703) 305-6983. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, OLIK CHAUDHURI can be reached on (703) 308-2794. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

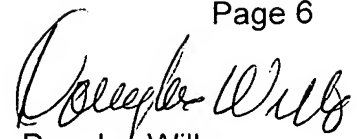
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DLN

April 5, 2002

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A handwritten signature in black ink, appearing to read "Douglas Wille", written in a cursive style.

Douglas Wille

Patent Examiner